Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2578	(Pixels) same (memory or capacitors or latches)same (D/A or DAC or DA)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 08:49
L2	864	(Pixels) with (memory or capacitors or latches)with (D/A or DAC or DA)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 08:49
L3	1	(Pixels)near (memory or capacitors or latches)near (D/A or DAC or DA)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 08:50
L4	0	(Pixels)near (memory or capacitors or latches)near (D/A or DAC or DA)same (gate near lines)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 08:51
L5	19	(Pixels)with (memory or capacitors or latches)with (D/A or DAC or DA)same (gate near lines)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 08:55
L6	1	(Pixels)near (memory or capacitors or latches)near (D/A or DAC or DA)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 09:00
L7	1	(Pixels)near (memor\$4 or capacitors or latches)near (D/A or DAC or DA)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 09:00

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2578	(Pixels) same (memory or capacitors or latches)same (D/A or DAC or DA)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 08:49
L2	864	(Pixels) with (memory or capacitors or latches)with (D/A or DAC or DA)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 08:49
L3	1	(Pixels)near (memory or capacitors or latches)near (D/A or DAC or DA)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 08:50
L4	0	(Pixels)near (memory or capacitors or latches)near (D/A or DAC or DA)same (gate near lines)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 08:51
L5	19	(Pixels)with (memory or capacitors or latches)with (D/A or DAC or DA)same (gate near lines)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 08:55
L6	1	(Pixels)near (memory or capacitors or latches)near (D/A or DAC or DA)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 09:00
L7	1	(Pixels)near (memor\$4 or capacitors or latches)near (D/A or DAC or DA)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON .	2005/04/05 09:38
L8	2	("5945972").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/05 09:41
L9	2	("6774876").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/05 09:42
L10	2	("6765562").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/05 09:43

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L11	2	("6750836").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/05 09:44
L12	2	"6738054"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 09:45
L13	366	(pixel) with (memory or capacitor or latch)with (DAC or DA)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 09:46
L14	33	(pixel) near (memory or capacitor or latch)with (DAC or DA)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 09:56
L15	41	(pixel) near (memor\$4 or capacitor\$3 or latch\$3)with (DAC or DA)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 10:05
L16	370	(pixel) with (memor\$4 or capacitor\$3 or latch\$3)with (DAC or DA)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 10:06